

# Abstracts

## A New Digital Phase Shifter Architecture Suitable for MMIC's (Short Papers)

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*J. Magarshack. "A New Digital Phase Shifter Architecture Suitable for MMIC's (Short Papers)." 1994 Transactions on Microwave Theory and Techniques 42.1 (Jan. 1994 [T-MTT]): 154-156.*

A new architecture is proposed for digital phase shifters that is suitable for monolithic integration (MMIC's). It consists of an arrangement of a standard building block which is a vector summer and timer repeated many times over the circuit. Examples of phase shifters up to 6 b will be given. Other arrangements of these blocks will be mentioned, which give rise to new possibilities.

[Return to main document.](#)